

7/58356

C O F C

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Dated: May 19, 2008

Signature:

Martha F. Thole
MARTHA F. THOLE

Docket No.: 42085.00031D2
(PATENT)



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Yong Shen, Kwok Kam Leung, Hiroshi Kiyono and
Testsuo Miyazaki

Application No.: 10/792078

Group Art Unit: 2627

Filed: March 3, 2004

Examiner: Brian E. Miller

For: ELIMINATION OF ELECTRIC-POP NOISE
IN A MR DEVICE

REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322

MS: Post Issue
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
MAY 27 2008
of Correction

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted the following errors which should be corrected.

Patent

Application File

Column 3, Line 20

Insert -20—between "layer" and "varies" as in the Clean Substitute Specification provided in the response dated January 11, 2006

Column 3, Line 42

Insert -ABS—after "Numeral 60" as in the Amendment dated August 4, 2006

Column 3, Line 56

Insert -MR—after "the" as in the Amendment dated August 4, 2006

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Column 4, Line 40

Replace $\frac{R_{SAL} = L_{SAL}}{R_{MR} + W_{MR} + R_{SAL} + L_{SAL}}$
with $\frac{R_{SAL}}{(R_{MR} + R_{SAL})}$: voltage shunting factor,
as in the original application

Column 4, Line 65

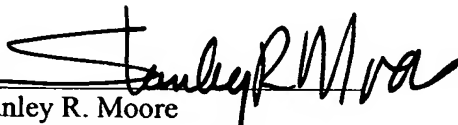
Replace $\frac{R_{SAL}}{(R_{MR} + R_{SAL})}$: voltage shunting factor,
with $\frac{R_{SAL} = L_{SAL}}{R_{MR} + W_{MR} + R_{SAL} + L_{SAL}}$
as in the original application

The errors were made by the U.S. Patent Office; accordingly, no fee is due.

Transmitted herewith is a proposed Certificate of Correction, in duplicate, effecting such amendment. Patentee respectfully solicits the granting of the requested Certificate of Correction.

Dated: May 17, 2008

Respectfully submitted,

By 
Stanley R. Moore
Registration No.: 26,958
WINSTEAD PC
P. O. Box 50784
Dallas, Texas 75250-0784
(214) 745-5110
Attorneys For Applicant

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,158,356 B2
 DATED : January 2, 2007
 INVENTOR(S) : Yong Shen, Kwok Kam Leung, Hiroshi Kiyono and Tetsuo Miyazaki

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 3, LINE 20

Please insert --20--between "layer" and "varies"

COLUMN 3, LINE 42

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COLUMN 3, LINE 56

Please insert --MR--after "the"

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COLUMN 4, LINE 65

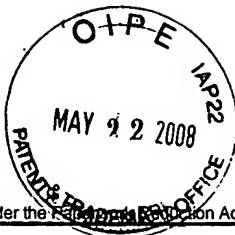
Replace $\frac{R_{SAL}}{(R_{MR} + R_{SAL})}$: voltage shunting factor, with $\frac{R_{SAL} = L_{SAL}}{R_{MR} + W_{MR} + R_{SAL} + L_{SAL}}$

MAILING ADDRESS OF SENDER:
 Stanley R. Moore
 WINSTEAD P.C.
 P. O. Box 50784
 Dallas, Texas 75250-0784

PATENT NO 7,158,356 B2

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PTO/SB/92 (09-04)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Application No. (if known): 10/549836

Attorney Docket No.: 27793.00102USPX

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Certificate of Correction (1 page)

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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INVENTOR(S) : Yong Shen, Kwok Kam Leung, Hiroshi Kiyono and Tetsuo Miyazaki

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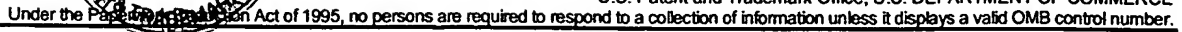
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Attorney Docket No.: 27793.00102USPX

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